

Notice of References Cited	Application/Control No. 09/927,204	Applicant(s)/Patent Under Reexamination FOLTIN ET AL.	
	Examiner Thomas H. Stevens	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,581,197 B1	06-2003	Foutz et al.	716/6
	B	US-6,604,227 B1	08-2003	Foltin et al.	716/6
	C	US-6,609,233 B1	08-2003	Foltin et al.	716/6
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chen et al., "A New Framework for Static Timing Analysis, Incremental Timing Refinement, and Timing Simulation" IEEE Dec. 2000 pg. 102-107
	V	Huelsbergen et al., "Using the Run-Time Sizes of Data Structures to Guide Parallel-Thread Creation" ACM Conference 1994. pg. 1-12
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.